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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Jeffrey S. Mailloux et al.	Examiner: Hong Kim
Serial No.:	08/984,563	Group Art Unit: 2751
Filed:	December 3, 1997	Docket: 303.623US4
Title:	ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION	

PRELIMINARY AMENDMENT

Box CPA
Assistant Commissioner for Patents
Washington, D.C. 20231

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Before taking up the present application for examination, please amend the application as follows:

IN THE CLAIMS

Please amend the claims as follows:

36. (Amended) A method for accessing an asynchronously-accessible dynamic random access memory, comprising [the steps of]:

receiving an external row address to the asynchronously-accessible dynamic random access memory;

selecting between a burst mode and a pipelined mode of operation of the asynchronously-accessible dynamic random access memory;

selecting between a read operation and a write operation of the asynchronously-accessible dynamic random access memory; and

obtaining a first external column address for accessing the asynchronously-accessible dynamic random access memory.

37. (Amended) The method, as in Claim 36, further comprising [a step of]:

obtaining a second external column address subsequent to the first external column address for operation in the pipelined mode.